

<b>Notice of References Cited</b>	Application/Control No. 09/552,383	Applicant(s)/Patent Under Reexamination WILLIS, STEPHEN L.	
	Examiner José R. Díaz	Art Unit 2815	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,741,626 A	04-1998	Jain et al.	430/314
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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	N					
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	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al. ,Silicon Processing for the VLSI Era, Volume 1: Process Technology, Lattice Press, 1986, pages 189-191.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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